

Advantages of Huawei 384 Optical Module



Overview

Huawei's CloudMatrix 384 Supernode outperforms Nvidia's 180 PFLOPs NVL72 with 300 PFLOPs, higher memory bandwidth, and sanction-proof design, fueling China's AI infrastructure ambitions. Offering ultra-high bandwidth, ultra-low latency, and exceptional performance, it demonstrates exceptional performance in tasks such as training and inference of large AI. At the Huawei Cloud Ecosystem Conference 2025, the CloudMatrix384 (CM384) Super Node once again captured significant attention across the industry. However, publicly available information about this super node remains relatively limited. Based on currently known materials, this article provides an. In the AI era, Huawei provides a full range of GE to 800GE optical modules, featuring three major capabilities: Spanning (ultra-long transmission), Stable (ultra-high reliability), and Secure (ultra-solid security). The AI system employs a "supernode" architecture with high-speed internal chip interconnects. 2% failures stem from optics & how QSFPTEK cuts costs by 69. On May 14, 2025, the "2025 Chip and Optical Forum" hosted by HiSilicon and organized by. · Efficiency Disadvantage: Exhibits 2. 8× worse bandwidth efficiency, and 1. · Costlier and Heavier to Support: Priced at ~\$8 million (~RMB 60 million), versus ~\$3 million for NVL72.



Article Content

Huawei CloudMatrix 384 Super-node: 6912×400G SiPh LPO Optics

Deep dive into Huawei's AI super-node: 384 Ascend 910C chips, 6912×400G OSFP SiPh LPO modules (1:18 ratio), 1.7x faster than NVIDIA. Learn why 68.2% failures stem from optics & how

Huawei's new AI CloudMatrix cluster beats Nvidia's

The CloudMatrix 384 is an enterprise -grade machine that features fault-tolerant capabilities and is designed for scalability.

Inside Huawei CloudMatrix 384: 2xThroughput of NVIDIA GB200

The Huawei CloudMatrix 384 is a bold, strategic counterpunch to NVIDIA, proving that domestic engineering, scale, and optical networking can offset per-chip performance disadvantages.

Optical Modules in Intelligent Computing Scenarios

In the AI era, Huawei provides a full range of GE to 800GE optical modules, featuring three major capabilities: Spanning (ultra-long transmission), Stable (ultra-high reliability), and Secure (ultra-solid

Huawei's SuperNode CloudMatrix384: Networking Advantages from

Huawei CloudMatrix384 (CM384) is a large-scale AI super node developed by Huawei that integrates 384 Ascend 910C NPUs into a single computing system. It is designed for high

Huawei launches CloudMatrix 384 AI System to rival

According to Huawei, the Ascend AI chip-based CloudMatrix 384 with three important benefits: These three perks can help enterprises achieve better

Huawei's CloudMatrix 384 Supernode: How This 300 PFLOP Monster

Huawei's CloudMatrix 384 Supernode outperforms Nvidia's 180 PFLOPs NVL72 with 300 PFLOPs, higher memory bandwidth, and sanction-proof design, fueling China's AI infrastructure

Huawei AI CloudMatrix 384 – China's Answer to Nvidia GB200 NVL72

Meet China's newest and most powerful Chinese domestic solution, the CloudMatrix 384 built using the Ascend 910C. This solution competes directly with the GB200 NVL72, and in some

Huawei releases 384 Ascend supernode technology

The Ascend 384 supernode uses an all-optical interconnect architecture (3,168 optical fibers + 6,912 400G optical modules), replacing traditional copper cables to achieve long-distance, high

Huawei launches CloudMatrix 384 AI System to rival Nvidia's most ...

According to Huawei, the Ascend AI chip-based CloudMatrix 384 with three important benefits: These three perks can help enterprises achieve better AI training as well as stable

Huawei's SuperNode CloudMatrix384: Networking

Huawei CloudMatrix384 (CM384) is a large-scale AI super node developed by Huawei that integrates 384 Ascend 910C NPUs into a single

Huawei CloudMatrix 384: China's AI Powerhouse vs Nvidia

Discover how Huawei's CloudMatrix 384 AI supercomputer outpaces Nvidia's GB200 NVL72 with 384 Ascend chips, massive bandwidth, and optical interconnect.

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